

Abstract

A power limiter for limiting power of high frequency signals at an input to a receiver comprises a plurality of transmission line sections connected in succession. Each section has a series inductance coupling an input to an output of the section, and a shunt capacitance constituted by capacitance of at least one pair of oppositely-poled Schottky diodes coupled at the output of the section to limit voltage of the signal at the output. Individual diodes can be replaced by series-connected diodes, or by an array of parallel and series-connected diodes, in different sections for improved performance of the limiter. The limiter can be integrated with a GaAs low noise amplifier of the receiver.